

Substitute for form 1449A/PTO (modified)

INFORMATION DISCLOSURE STATEMENT BY APPLICANT



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Page 1 of 2

Application Number	10/076174
Filing Date	February 14, 2002
First Named Inventor	Baude, Paul F.
Art Unit	1756 2823
Examiner Name	M. Estrada
Attorney Case Number	57322US002

U.S. Patent Documents

Exam. Init.*	Cite No.	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Doc. Number-(Kind Code if Known)			
	A1	US- 3,669,060	06/13/72	Page et al.	
	A2	US- 4,096,821	06/27/78	Greeneich et al.	
	A3	US- 4,335,161	06/15/82	Luo	
	A4	US- 4,676,193	06/30/87	Martin	
	A5	US- 4,746,548	05/24/88	Boudreau et al.	
	A6	US- 4,915,057	04/10/90	Boudreau et al.	
	A7	US- 4,963,921	10/16/90	Kariya et al.	
	A8	US- 5,397,607	03/14/95	Shields et al.	
	A9	US- 6,087,196	07/11/00	Sturm et al.	
	A10	US- 6,106,627	08/22/00	Yializis	
	A11	US- 6,285,001 B1	09/04/01	Fleming et al.	

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		Ctry. Code	Number-KindCode (if known)				
	B1	EP	0 118 576	12/02/87			
	B2	EP	0719 638 A2	07/03/96			
	B3	JP	5-228669	09/07/93			X
	B4	JP	9-27454	01/28/97			X
	B5	PCT	WO 99/30336	06/17/99			
	B6	PCT	WO 99/39373	08/05/99			
	B7	PCT	WO 99/54786	10/28/99			

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Exam. Init.*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
	C1	Brody, "Large Area Masking Techniques for Thin Film Transistor Arrays," SPIE, Vol. 100, Semiconductor Microlithography II, pp. 140-150(1977)
	C2	Weimer, "The TFT- A New Thin-Film Transistor," Proceedings of the IRE, pp. 1462-1469(1962).
	C3	Brody et al., "Flexible Thin-Film Transistors Stretch Performance, Shrink Cost," Research, pp. 20-23 (1969).

*Examiner: *Michelle Estrada*

Date Considered:

10/28/02

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JCP Page 2 of 2

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Examiner Name M. Estrada

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ME	C4	Schon et al., "Fast Organic Electronic Circuits Based on Ambipolar Pentacene Field-Effect Transistors," Applied Physics Letters, Vol. 79, No. 24, pp. 4043-4044 (2001).
ME	C5	Gouy et al., "Deposition of Organic Semiconductor Through Silicon Shadow Masks for Integrated Led on Silicon."
ME	C6	Kim et al., "Photoplastic Shadow-Masks for Rapid Resistless Multi-Layer Micropatterning".
ME	C7	Kim et al., "Dry-Lift-Off Patterning on Fragile Surfaces Using MEMS Shadow Masks," pp. 619-622.

*Examiner: Michelle Estrada

Date Considered: 10/28/03

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	A2	US-			T 1
	A3	US-			13 17
	A4	US-			13 17
	A5	US-			13 17
	A6	US-			13 17
	A7	US-			13 17
	A8	US-			13 17
	A9	US-			13 17
	A10	US-			13 17
	A11	US-			13 17

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		Ctry. Code	Number-KindCode (If known)				
PFF	B1	PCT	WO 99/09456	02/25/99	Pierrat		
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	B3						
	B4						
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	B6						
	B7						

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	C2	
	C3	

*Examiner: *Michele Estrada*Date Considered: *10/28/03*

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Examiner Name	Unknown M. Estrada
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A1	US- 2002/0062785A1	05/30/2002	Kim et al.	TC 2830 MAIL ROOM	RECEIVED DEC 23 2002
A2	US-				
A3	US-				
A4	US-				
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A7	US-				
A8	US-				
A9	US-				
A10	US-				
A11	US-				

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B4							
B5							
B6							
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	C1	
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<i>ME</i>	A1	US- 5,626,784	05/06/1997	Simons	
	A2	US-			
	A3	US-			
	A4	US-			
	A5	US-			
	A6	US-			
	A7	US-			
	A8	US-			
	A9	US-			
	A10	US-			
	A11	US-			

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		Ctry. Code	Number-KindCode (If known)				
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<i>ME</i>	B2	GB	2 276 589 A	10/05/1994	David Godfrey Williams		
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	B4						
	B5						
	B6						
	B7						

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